



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.: 10/085,432

Filed: February 28, 2002

Inventor(s):

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Lavelle, Nandini Ramani and

Patrick Shehane

Title: ARBITRATION SCHEME
FOR EFFICIENT
PARALLEL PROCESSING

Examiner: Nguyen, Hau H.

Group/Art Unit: 2676

Atty. Dkt. No: 5181-87600

P6070

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the date indicated below.

Jeff C. Hood

Qc2

Signature

8/12/2004

Date _____

**RESPONSE TO OFFICE ACTION OF
AUGUST 11, 2004**

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

This paper is submitted in response to the Office Action of August 11, 2004, to further highlight why the application is in condition for allowance.

Please amend the case as listed below.

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